



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/064,916	08/29/2002	Ching-Fang Yen	7558-US-PA	9010
31561 7590 08/21/2009 JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE 7 FLOOR-1, NO. 100 ROOSEVELT ROAD, SECTION 2 TAIPEI, 100 TAIWAN			EXAMINER PEUGH, BRIAN R	
			ART UNIT 2187	PAPER NUMBER
			NOTIFICATION DATE 08/21/2009	DELIVERY MODE ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

USA@JCIPGROUP.COM.TW
Belinda@JCIPGROUP.COM.TW

Office Action Summary

Application No.

10/064,916

Applicant(s)

YEN ET AL.

Examiner

Brian R. Peugh

Art Unit

2187

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 28 July 2009.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 and 3-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,3-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-946)
- 3) ☐ Information Disclosure Statement(s) (PTO/SE/US)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

Response to Amendment

This Office Action is in response to applicant's communication filed July 28, 2009 in response to PTO Office Action dated April 28, 2009. The applicant's remarks and amendment to the specification and/or claims were considered with the results that follow.

Claims 1 and 3-7 have been presented for examination in this application. In response to the last Office Action, claim 1 has been amended.

Drawings

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the "...wherein the multi-memory architecture has a pin configuration same as a standard pin configuration of the first type memory" must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for

consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1 and 3-7 rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Newly amended claim 1 recites "A multi-memory architecture comprising: a first type memory; and a second type memory; wherein the multi-memory architecture has a pin configuration same as a standard pin configuration of the first type memory...". It is unclear from Applicant's Specification where a multi-memory architecture has a same pin configuration as one of its internal components, that of the first type memory. Paragraph 0037 indicates that in Fig. 1, the Mask ROM has the same pin configuration

as the multi-memory architecture. This is also what is now claimed. However, the (WP)' and (RESET)' signals of the multi-memory architecture are not part of the Mask ROM. Therefore it is unclear to the Examiner where in Applicant's Specification there is to be found support for the currently amended claim language.

Allowable Subject Matter

Due to the nature of the above 35 U.S.C. 112, 1st paragraph rejection noted above, it is unclear to the Examiner whether Applicant's claim language contain allowable subject matter.

Response to Arguments

Applicant's arguments filed July 28, 2009 have been fully considered but they are not persuasive.

Applicant has argued on page 4 of the response that:

In response thereto, Applicants respectfully submit that, referring to paragraph [0052] of the specification, the present application provides a multi-memory architecture comprising two different types of memory devices, but the overall pin configuration is the same as one of the memory devices. That actually means the new multi-memory architecture has the same pin configuration as a standard pin configuration of one of the memory device disposed therein. As a result, the new multi-memory architecture can "function" as that memory device ordinarily does, so as to eliminate compatibility problem. Therefore, although the (WP)' and (RESET)' pins of the multi-memory architecture shown in FIG. 1 are not part of the Mask ROM die 100, they are truly part of a conventional Mask ROM package, so as to make the multi-memory architecture of the present application compatible with all the applications for the conventional Mask ROM package.

The Applicant appears to be demonstrating that support for the claim limitation in question, relating architecture pin configuration to first type memory pin configuration.

The Applicant has indicated that "...overall pin configuration is the same as one of the memory devices", as well as "...Therefore, although the (WP)' and (RESET)' pins of the multi-memory architecture shown in FIG. 1 are not part of the Mask ROM die 100, they are truly part of a conventional Mask ROM package, so as to make the multi-memory architecture of the present application compatible with all the applications for the conventional Mask ROM package.". The Applicant has indicated that the Mask ROM die (100) is part of a conventional Mask ROM package, however the Examiner is unclear of any recitation in Applicant's Specification or Drawings that describes the composition of a conventional Mask ROM package.

Also, the Applicant has indicated that "...but the overall pin configuration is the same as one of the memory devices. That actually means the new multi-memory architecture has the same pin configuration as a standard pin configuration of one of the memory device disposed therein". The Examiner notes that the Applicant has indicated that the pin configuration is the same at first, while also indicating that the pins are not found on the Mask ROM die 100 but are part of a different conventional Mask ROM package. As noted supra, the Specification and Drawings do not appear to fully describe the composition of a conventional Mask ROM package.

Therefore, it appears that the claim limitation "...multi-memory architecture has a pin configuration same as a standard pin configuration of the first memory type" does

not appear to have the necessary Specification support, but rather that the architecture pin configuration includes the pin configuration of a first type memory.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian R. Peugh whose telephone number is (571) 272-4199. The examiner can normally be reached on Monday-Thursday from 7:00am to 4:30pm. The examiner can also be reached on alternate Friday's from 7:00am to 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Christian Chace, can be reached on (571) 272-4190. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 571-272-2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Application/Control Number: 10/064,916
Art Unit: 2187

Page 7

/Brian R. Peugh/
Primary Examiner, Art Unit 2187